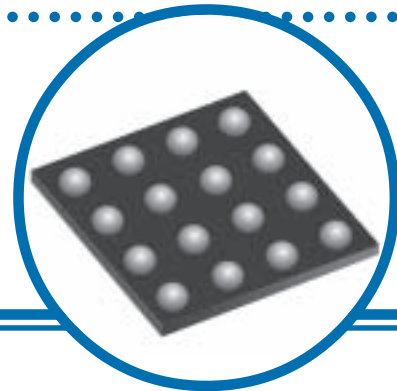


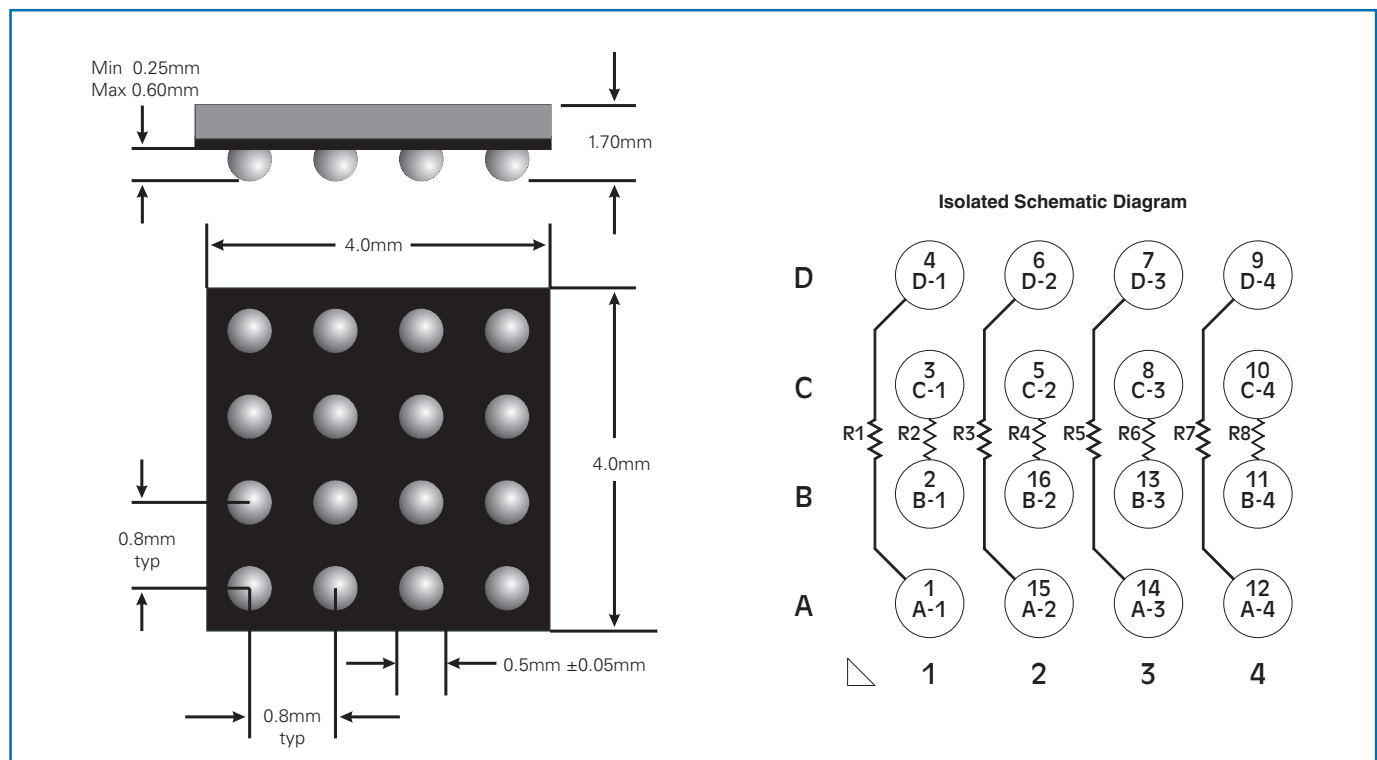
# Molded Chipscale Package Termination Array

- Thin film on silicon technology
- High density with reduced footprint
- Excellent solution for thin, portable products
- Compliant and reworkable - No underfill required



IRC now offers wafer level packaged components integrating tantalum nitride resistors, thin film capacitors and/or diodes in an application specific circuit. Based on IRC's proven TaNCap® Technology, high density packages are available to suit the most demanding high density applications. IRC's chipscale packaging offers smaller sizes than traditional SOIC and QSOP packages while still offering solder bump or compliant lead connections.

## Outline Dimensions and Schematic Diagram



### General Note

IRC reserves the right to make changes in product specification without notice or liability. All information is subject to IRC's own data and is considered accurate at time of going to print.

# Molded Chipscale Package Termination Array



## Electrical Data

| Absolute Tolerance | Absolute TCR                       | Tracking TCR                      | Element Power Rating | Package Power Rating | Operating Temperature                      | Voltage Rating |
|--------------------|------------------------------------|-----------------------------------|----------------------|----------------------|--|----------------|
| to $\pm 1\%$       | $\pm 250\text{ppm}/^\circ\text{C}$ | $\pm 10\text{ppm}/^\circ\text{C}$ | 100mW                | 500mW                | $-40^\circ\text{C}$ to $+85^\circ\text{C}$ | 50V            |

## Ordering Procedure

Prefix . . . . . **CHM** - **MD0450A** - **01** - **51R0** - **J**

Model . . . . .  
4 X 4 array, isolated schematic

Absolute TCR Code . . . . .  
99 = 250ppm/ $^\circ\text{C}$

Four Digit Resistance Code . . . . .  
Standard Resistance Values  
22R0 = 22 $\Omega$ ; 33R0 = 33 $\Omega$ ; 47R0 = 47 $\Omega$ ; 50R0 = 50 $\Omega$   
51R1 = 51 $\Omega$ ; 75R0 = 75 $\Omega$ ; 1000 = 100 $\Omega$

Absolute Tolerance Code . . . . .  
J =  $\pm 5\%$ ; F =  $\pm 1\%$

For additional information or to discuss your specific requirements, please contact our Applications Team using the contact details below.