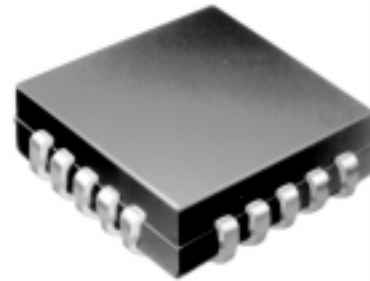




TANFILM™ PLASTIC LEADED CHIP CARRIER NETWORK

PLCC SERIES



- Thin film precision
- High resistor density - 0.05" lead spacing
- 20 and 28 pin size available
- Standard JEDEC package type M0-047AA for automatic placement equipment
- Low cost and reliable

IRC's TanFilm™ Plastic Leaded Chip Carrier resistor networks are ideally suited for your surface mount applications. The 0.05 inch lead pitch and high lead count provides high resistor density in a compact, sturdy package. The "J" shaped compliant leads reduce layout space and relieve thermal stresses caused by soldering and temperature excursions between the printed wiring board and the component.

The proven TanFilm™ manufacturing process begins with in-house computer aided design for both standard and custom designs. Vacuum sputtering on a high purity alumina ceramic, exacting photolithography and precise laser trimming formulate the resistor network.

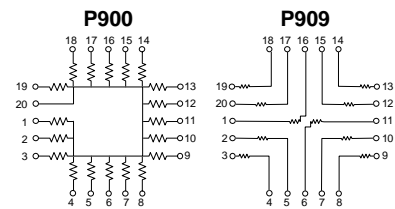
The Tantalum Nitride Film system provides precision tolerances, exceptional TCR tracking and low noise. TanFilm™ provides exceptional stability, high reliability, and long, predictable life characteristics.

For applications requiring resistor networks with a small physical size, low cost, high level of stability, lead compliance and high resistor density in a surface mount package, specify IRC PLCC resistor networks.

SPECIFICATIONS:

Resistance Range (ohms)	P900: 10 to 100K	P909: 10 to 200K
Resistance Tolerances	±0.1%, ±0.25%, ±0.5%, ±1%, ±2% available	
Ratio Accuracy:	to ±0.05%	
Temperature Coefficient of Resistance (ppm/°C):	±25, ±50, ±100	
TC Tracking:	±5 ppm/°C standard (±10 ppm/°C below 100 Ω) ±2 ppm/°C available	
Power Rating @ 70°C:	1W per package 0.08W per resistor for P900 0.15W per resistor for P909	
Noise	Less than -25 db	
Operating Temperature Range:	-55°C to +125°C	
Substrate:	99.5% pure alumina ceramic	
Lead Material:	Solder plated copper alloy	
Custom Circuits and Special Testing Available		

SCHEMATICS:



HOW TO ORDER:

Sample Part No.:

P90X XX XXXX X X

Schematic

0 - Bussed, 9 - Isolated

Characteristic

Temperature Coefficient Code, ppm/°C

01: ±100, 02: ±50, 03: ±25

Resistance

Standard MIL resistance code

Absolute Tolerance

Ratio tolerance to R₁ (if specified)

DIMENSIONS:

