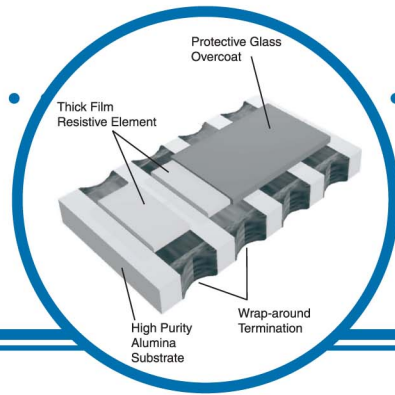


# Thickfilm Chip Array Resistor Network

## WCC Series

- Monolithic construction
- Concave termination style
- Non-leaching nickel barrier wrap-around terminations
- Improved placement efficiency over flat chip resistors



## Electrical Data

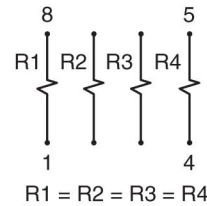
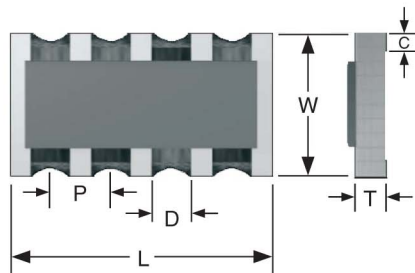
<b>Resistance Range</b>	1% 22Ω - 1MΩ 5% 10Ω - 1MΩ zero ohm jumper
<b>Operating Temperature Range</b>	-55°C to +125°C
<b>Temperature Coefficient</b>	±200ppm/°C
<b>Power Dissipation (@70°C)</b>	0.65 Watts
<b>Maximum Working Voltage</b>	50 Volts

## Environmental Data

Test*	Maximum ΔR
<b>Thermal Shock</b>	±1.0%
<b>Short Time Overload</b>	±1.0%
<b>High Temperature Exposure</b>	±3.0%
<b>Resistance to Solder Heat</b>	±1.0%
<b>Moisture Resistance</b>	±2.0%
<b>Load Life</b>	±3.0%
<b>Low Temperature Operation</b>	±0.5%
<b>Solderability</b>	95% Coverage

\* Test methods per EIA-575

## Physical Data



### Dimensions (Inches and (mm))

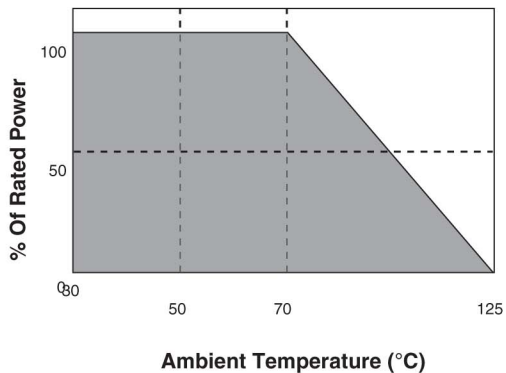
L	C	D	P	W	T
0.126 ± .008 (3.20 ± .20)	0.018 ± .004 (0.45 ± .10)	0.016 ± .006 (0.40 ± .15)	0.031 ± .008 (0.80 ± .20)	0.063 ± .008 (1.60 ± .20)	0.016 ± .004 (.40 ± .10)

### General Note

IRC reserves the right to make changes in product specification without notice or liability. All information is subject to IRC's own data and is considered accurate at time of going to print.

# Thickfilm Chip Array Resistor Network

## Power Derating Curve



## Ordering Data

